**East West University**

**Department of CSE**

**LAB REPORT**

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| --- | --- | --- |
| **Course Code and Name:**  CSE209 Electrical Circuits | | |
| **Datasheet no: 05** | | |
| **Experiment name:**  Verification of Superposition Theorem | | |
| **Semester and Year:**  Fall 2021 |  | |
| **Name of Student:**  D.M. Rafiun – Bin -Masud | **Course Instructor information:**  M Saddam Hossain Khan  Senior Lecturer, Department of Computer Science and Engineering | |
| **Student Id:**  2019-3-60-137 |
| **Date of Report Submitted:**  20 December,2021 | **Pre-Lab Marks:** |  |
| **Post Lab Marks:** |  |
| **TOTAL Marks:** |  |

**Title: Verification of Superposition Theorem**

**Experimental Datasheet:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Measured Value of *E*1 (V) | Measured Value of *E*2 (V) | Measured Value of *E*3 (V) | Measured value of  *IL* with all sources  active  (mA) | Measured value of *IL*1 with only *E*1 active  (mA) | Measured value of *IL*2 with only *E*2 active  (mA) | Measured value of *IL*3 with only *E*2 active  (mA) | Measured values of  resistors  () |
| 10 | 5 | 5 | -4.269 | 12.16 | 14.73 | -31.168 | *R*1 = 33  *R*2 = 47  *R*3 = 33  *R*4 = 47  *R*5 = 47  *RL* =68 |

